



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1430
 Alexandria, Virginia 22313-1450
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 1735

SERIAL NUMBER 10/631,093	FILING DATE 07/31/2003 RULE	CLASS 257	GROUP ART UNIT 2823	ATTORNEY DOCKET NO. SC12885TP
-----------------------------	-----------------------------------	--------------	------------------------	-------------------------------------

APPLICANTS

Marius K. Orlowski, Austin, TX;

Leo Mathew, Austin, TX;

None bok

** CONTINUING DATA *None bok*** FOREIGN APPLICATIONS *None bok*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 10/29/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY TX	SHEETS DRAWING 6 ✓	TOTAL CLAIMS 25 13	INDEPENDENT CLAIMS -3-1
Verified and Acknowledged Examiner's Signature <i>Bu Rensu</i>	Initials <i>MR</i>				

ADDRESS

23125
 FREESCALE SEMICONDUCTOR, INC.
 LAW DEPARTMENT
 7700 WEST PARMER LANE MD:TX32/PL02
 AUSTIN , TX
 78729

TITLE

Method of forming a transistor having multiple channels and structure thereof

FILING FEE RECEIVED 840	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)
-------------------------------	---	--

Other _____
 Credit

THIS PAGE BLANK (USPTO)